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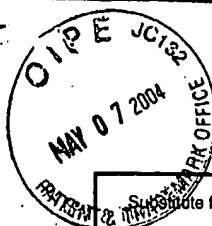
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 2

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| | |
|----------------------|-----------------|
| Application Number | 09/880,734 |
| Filing Date | June 12, 2001 |
| First Named Inventor | Croslan, Andrew |
| Art Unit | 2116 |
| Examiner Name | Tse W. Chen |

Attorney Docket Number 15114-053500US

| U.S. PATENT DOCUMENTS+ | | | | | |
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| Examiner Initials* | Cite No. ¹ | Document Number Number Kind Code ² (if known) | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
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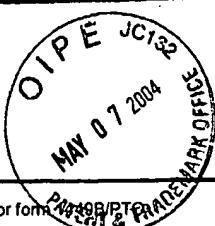
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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| | | | Complete If Known | |
|-------|---|----|-----------------------------|------------------------|
| | | | <i>Application Number</i> | 09/880,734 |
| | | | <i>Filing Date</i> | June 12, 2001 |
| | | | <i>First Named Inventor</i> | Crosland, Andrew |
| | | | <i>Art Unit</i> | 2116 |
| | | | <i>Examiner Name</i> | Tse W. Chen |
| Sheet | 2 | of | 2 | Attorney Docket Number |
| | | | 15114-053500US | |

NON PATENT LITERATURE DOCUMENTS

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|---------------------|-----------------------|---|------------------------|
| TWC | AO | DeHON, DPGA-Coupled Microprocessors: Commodity ICs for the Early 21st Century, Artificial Intelligence Laboratory, Massachusetts Institute of Technology, Cambridge, MA, IEEE, February, 1994, pp. 31-33 | |
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| TWC | AU | WYNN, "In-Circuit Emulation for ASIC-Based Designs," Xilinx, Inc., VLSI Systems Design, October, 1986, pp. 38-39, and 42-45. | |
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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
Stylesheet Version v18.0

| | | | | | | | |
|---|---|------------|------------|-----------|------|-------|----------|
| Title of Invention | Embedded processor with watchdog timer for programmable logic | | | | | | |
| Application Number: | 09/880734 | | | | | | |
| Confirmation Number: | 4950 | | | | | | |
| First Named Applicant: | Andrew Crosland | | | | | | |
| Attorney Docket Number: | 015114-053500US | | | | | | |
| Art Unit: | 2116 | | | | | | |
| Examiner: | Tse W Chen | | | | | | |
| Search string: | (5687325 or 6260087).pn. | | | | | | |
| US Patent Documents | | | | | | | |
| Note: Applicant is not required to submit a paper copy of cited US Patent Documents | | | | | | | |
| init | Cite.No. | Patent No. | Date | Patentee | Kind | Class | Subclass |
| TAK | 1 | 5687325 | 1997-11-11 | Web Chang | | | |
| TAK | 2 | 6260087 | 2001-07-10 | Web Chang | | | |
| Signature | | | | | | | |
| Examiner Name | | | | Date | | | |
| TSE CHEN | | | | 9/23/04 | | | |



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

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|--------------------|---|
| Title of Invention | Embedded processor with watchdog timer for programmable logic |
|--------------------|---|

Application Number: 09/880734



Confirmation Number: 4950

First Named Applicant: Andrew Crosland

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Attorney Docket Number: 015114-053500US

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Art Unit: 2116

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Examiner: Tse W. Chan

Search string: (6467009 or RE34444 or 5970254).pn.

US Patent Documents

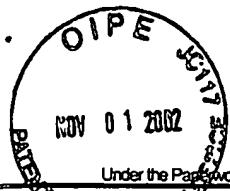
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| init | Cite.No. | Patent No. | Date | Patentee | Kind | Class | Subclass |
|------|----------|------------|------------|------------|------|-------|----------|
| TUE | 1 | 6467009 | 2002-10-15 | Winegarden | | | |
| TUE | 2 | RE34444 | 1993-11-16 | Kaplinsky | | | |
| TUE | 3 | 5970254 | 1999-10-19 | Cooke | | | |

Signature

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|---------------|---------|
| Examiner Name | Date |
| TSE CHAN | 9/23/04 |

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|-------------------------------|---------------------------|
| Application Number | 09/880,734 |
| Filing Date | June 12, 2001 |
| First Named Inventor | Crosland, Andrew, et. al. |
| Art Unit | 2819 |
| Examiner Name | To Be Assigned |
| Attorney Docket Number | 015114-053500US |

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Sheet

1 of 1

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|------------------------|-----------------|
| Application Number | 09/880,734 |
| Filing Date | June 12, 2001 |
| First Named Inventor | Croslan, Andrew |
| Group Art Unit | 2819 |
| Examiner Name | Unassigned |
| Attorney Docket Number | 015114-053500US |

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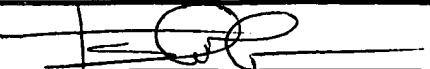
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| tvc | 01 | "Triscend E5 Configurable System-on-Chip Family," Product Description from Triscend Corporation, January, 2000 (Version 1.00), pp. i-ii and 1-90. | |
| tvc | 02 | "Motorola Technical Developments," Magazine of Motorola, Inc., Vol. 39, September 1999, pp. i-vii and 77-80. | |
| tvc | 03 | "AT94K Series Field Programmable System Level Integrated Circuit," Advance Information Brochure of Atmel Corporation, December 1999, 6 pages. | |
| tvc | 04 | "CS2000 Reconfigurable Communications Processor Family Product Brief," Advance Product Information from ChameleonSystems, Inc., 2000, pages 1-8 | |
| tvc | 05 | "Wireless Base Station Design Using Reconfigurable Communications Processors," Wireless Base Station White paper from ChameleonSystems, Inc., 2000, pages 1-8. | |
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Sheet

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|------------------------|---------------------------|
| Application Number | 09/880,734 |
| Filing Date | June 12, 2001 |
| First Named Inventor | Crosland, Andrew, et. al. |
| Art Unit | 2819 |
| Examiner Name | To Be Assigned |
| Attorney Docket Number | 015114-053500US |

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| TWC | 0015 | AITKEN, R.C., and AGARWAL, V.K., "A Diagnosis Method Using Pseudo-Random Vectors Without Intermediate Signatures," Proc. of Int. Conf. on Computer-Aided Design (ICCAD), IEEE pp. 574-577 (1989). |
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| TWC | 0023 | MC ANNEN, M.G. and SAVIR, J., "There is Information in Faulty Signatures," Proc. of International Test Conf., IEEE, pp. 630-636 (1987). |
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